



Prof. P. Chandra Sekhar

M. Tech, Ph.D., SMIEE, LIE, FIETE

Professor in ECE & Principal

University College of Engineering (Autonomous)

Osmania University, Hyderabad, Telangana, INDIA - 500007



Advertisement for Project Associate - I position in the MeitY sponsored Project under Chips to Startup Scheme in the Department of Electronics & Communication Engineering, University College of Engineering, Osmania University

Applications are invited from eligible candidates to work as JRF/Project Associate in the MeitY sponsored Project under Chips to Startup (C2S) Scheme titled "The Design, Fabrication and development Silicon Proven IP Core for High Resolution AD-PLL" in the Department of Electronics & Communication Engineering, University College of Engineering, Osmania University

Number of positions available: ONE (1)

Aim of the Project: Aim of the project is to Design, Fabricate and Develop silicon proven, state of art, IP Core for high resolution All Digital Phase Locked Loop ADPLL suitable for Multipurpose Low Energy Applications.

For more information regarding the project, visit: <http://www.uceou.edu/pdfs/faculty/50022-ADPLL-IP-Core-C2S-MeitTy.pdf>

Qualification:

- ME/ M.Tech. in Electronics and Communication Engineering /VLSI / or related area with first class.
- Preference will be given to GATE/NET qualified candidates.
- Experience in Verilog, Verilog-A, Analog IC Design and Cadence Virtuoso will be given preference.

Fellowship:

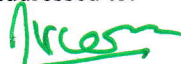
- Rs. 35,000 per month + HRA as per norms (for project tenure of maximum four years) for GATE/NET qualified candidates
- Rs. 31,000 per month + HRA as per norms (for project tenure of maximum four years) for other candidates

General Term and Condition: The position is temporary and renewable each year subject to satisfactory performance for a maximum of three and half years. The position is co-terminus with the project. The age limit is 35 years (5 years' relaxation for SC/ST/OBC/ female/Physically Handicapped Candidate).


Completely filled, signed application form must be submitted as per the template provided (Annexure – I). Original documents of age proof/certificates/degrees/mark sheets and other testimonials must be presented at the time of interview. Attested photocopies of these documents will be collected at the time of interview. Only shortlisted candidates will be communicated to appear in the interview and no other communications in this regard will be entertained.

No TA/DA will be paid for attending the interview. **(Interview Date will be intimated in website)**

Completed applications should reach **on or before 29th December, 2025** via email attachment addressed to:


Prof. P. Chandra Sekhar
Chief Investigator
Professor, Dept. of ECE & Principal
University College of Engineering, Osmania University
E-mail: chandrasedkhar.p@uceou.edu

 **+91 9866695963, 8331997292**

 **sekharp@osmania.ac.in, sekharpaidimarry@gmail.com**

Chief Investigator, IP Core: ADPLL C2S Program, MeitY & Principal Investigator, GNSS Receiver, MathWorks
<http://linkedin.com/in/prof-chandra-sekhar-paidimarry-808165237>



Estd.1917

University College of Engineering

(AUTONOMOUS)

OSMANIA UNIVERSITY, HYDERABAD – 500 007



Estd.1929

**APPLICATION FORM FOR POST OF PROJECT ASSOCIATE-I IN THE MEITY
SPONSORED PROJECT UNDER CHIPS TO STARTUP (C2S) SCHEME IN THE DEPT.
OF ELECTRONICS & COMMUNICATION ENGINEERING**

1. Name in full (in BLOCK letters) :
2. Parent's Name :
3. Date of birth :
4. Present address for correspondence :

Mobile :

Email ID :

Paste a recent
passport size
photograph

5. GATE/CSIR/UGC Exam : Y / N
(If Yes then give the percentage mark/rank and year of passing)
6. Educational Qualifications : (From SSC) (add more rows as required)

Sl. No	Qualification	Board / University	Year of passing	Percentage (%)

7. Experience:

Name of Post	Name of Organization /Dept.	From	To	Nature of duties performed

8. Number of Publications in Journals / Conferences * :

9. Courses/Training in VLSI (provide proof):

DECLARATION

I hereby, undertake that all the details furnished above are true to the best of my knowledge and I am liable for any disciplinary action, if found otherwise. My candidature may kindly be considered for the above temporary / contractual / short - term project position as per the rules of UGC/DST/MietY.

Place:

Date:

Signature

INSTRUCTIONS:

1. The application form should be neatly typed and sent by e-mail so as to reach before the deadline.
2. *Enclose photocopies of certificates and reprints of published work.